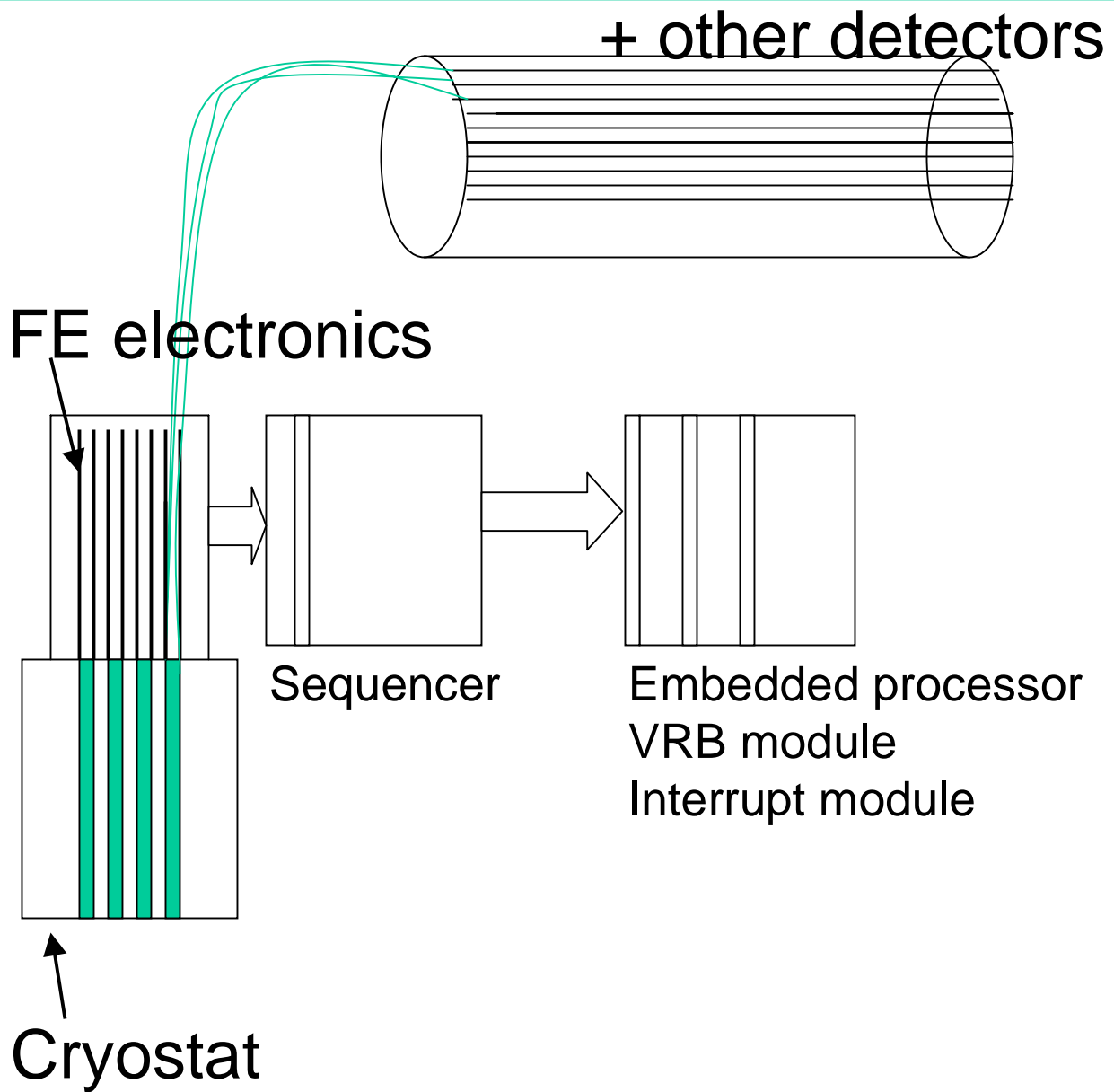


Fiber Tracker Setup at LAB 3



LAB 3 - Testing & Calibrating

- ❖ Cryostat houses 4096 VLPC pixels
- ❖ FE electronics:
 - 64 SVX chips
 - DAC chips for setting up bias voltage on VLPC chips
 - DAC chips for setting up threshold on SIFT chips
- ❖ Plan to Start in Nov '98

Software For LAB 3

- ❖ Have a PC System that they can use but, plan to use DART and EPICS for Data Acquisition and Control and Monitoring
- ❖ EPICS
 - 1553 support is needed for downloading SVX and DAC chips
 - ★ Generic record support in EPICS for 1553 is already in progress (F. Bartlett)
- ❖ DART
 - Interrupt driven readout (in the case of Cosmic Ray Trigger)
 - Reads out VRB module into UNIX box
- ❖ Plan is to grab Silicon code and modify and extend it for Fiber Tracker needs.
 - Fiber Tracker group will be starting to look at this software in the two weeks once hardware setup is in better shape
 - ★ No person designated to do this yet in the group.